

Electronic Acknowledgement Receipt

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Application Number:	10079472
International Application Number:	
Confirmation Number:	6706
Title of Invention:	Gate dielectric structures for integrated circuits and methods for making and using such gate dielectric structures
First Named Inventor/Applicant Name:	Maitreyee Mahajani
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Supplemental Appeal Brief	SD106US_FourthAppealBrief.pdf	5985649 <small>518031cc1050e8847ce865c885e630d29707297e</small>	no	37

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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